

	Type	Hits	Search Text	DBs	Time Stamp	C o m m e n t s	Error Definition	E r r o r s
64	BRS	7	(Nishizawa-Hiroataka.in. Ishihara-Haruji.in. Shiraishi-Atsushi.in. Kanemoto-Kouichi.in. Yukawa-Yousuke.in.) and card and @pd>20040305	USPAT; US-PGPUB; EPO; JPO	2004/07/29 10:30			0
65	BRS	382	(361/822.ccls. 361/763.ccls. 361/764.ccls. 439/60.ccls. 902/25.ccls. 902/26.ccls. 361/42.ccls. 340/7.32.ccls. 439/180.ccls. 439/630.ccls. 174/255.ccls. 257/679.ccls. 361/737.ccls. 235/441.ccls. 235/492.ccls.) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 10:46			0
66	BRS	0	((ic chip smart circuit memory) adj1 card\$1) same ((resin epox\$3 encapsul\$4 mould\$3) near5 (terminal\$1 contact\$3 pin\$1) near5 (cover\$3 cas\$3 body shell hous\$3)) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 13:04			0
67	BRS	3	((ic chip smart circuit memory) adj1 card\$1) same ((resin epox\$3 encapsul\$4 mould\$3) near5 (terminal\$1 contact\$3 pin\$1) near5 (cover\$3 cas\$3 body shell hous\$3)) and @pd>20040305	EPO; JPO; DERWENT; IBM_TDB	2004/07/29 13:05			0
68	BRS	20	((ic chip smart circuit memory) adj1 card\$1) same (resin epox\$3 encapsul\$4 mould\$3) same (terminal\$1 contact\$3 pin\$1) same (cover\$3 cas\$3 body shell hous\$3) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 13:11			0
69	BRS	8	((ic chip smart circuit memory) adj1 card\$1) same (resin epox\$3 encapsul\$4 mould\$3) same (terminal\$1 contact\$3 pin\$1) same (cover\$3 cas\$3 body shell hous\$3) and @pd>20040305	EPO; JPO; DERWENT; IBM_TDB	2004/07/29 16:26			0
70	BRS	2	((ic chip smart circuit memory) adj1 card\$1) same (parallel near5 serial near5 mode\$1) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 16:46			0

Type	Hits	Search Text	DBs	Time Stamp	Comments		Error
					Definition	Errors	
71	BRS 1	((ic chip smart circuit memory) adj1 card\$1) same (parallel near5 serial near5 (terminal\$1 contact\$1)) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 16:48			0
72	BRS 0	((ic chip smart circuit memory) adj1 card\$1) same (parallel near5 serial near5 (terminal\$1 contact\$1)) and @pd>20040305	EPO; JPO; DERWENT; IBM_TDB	2004/07/29 16:49			0
73	BRS 12	((ic chip smart) adj1 card\$1) same ((usb (universal adj serial adj bus)) near5 (terminal\$1 pin\$1 contact\$1)) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 16:50			0
74	BRS 1	((ic chip smart) adj1 card\$1) same ((usb (universal adj serial adj bus)) near5 (terminal\$1 pin\$1 contact\$1)) and @pd>20040305	EPO; JPO; DERWENT; IBM_TDB	2004/07/29 17:04			0
75	BRS 7	((ic chip smart circuit memory) adj1 card\$1) same ((communication near5 mode\$1) near5 (terminal\$1 pin\$1 contact\$1)) and @pd>20040305	USPAT; US-PGPUB	2004/07/29 17:05			0
76	BRS 0	((ic chip smart circuit memory) adj1 card\$1) same ((communication near5 mode\$1) near5 (terminal\$1 pin\$1 contact\$1)) and @pd>20040305	EPO; JPO; DERWENT; IBM_TDB	2004/07/29 17:10			0